

## Exercise – Hardware Setting

---

### Learning Outcomes

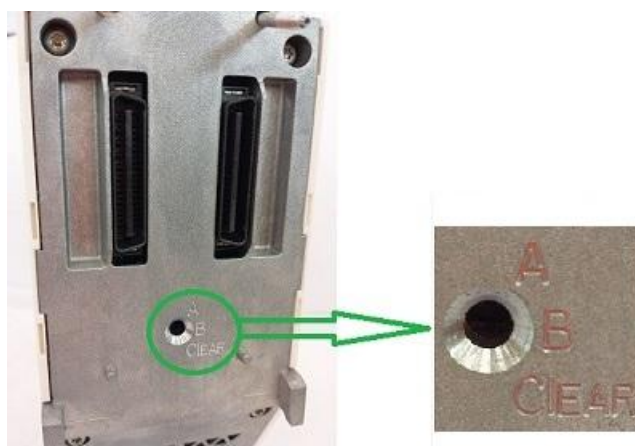
By the completion of this exercise you will:

- Configure the identity of the physical HSBY processor.
  - Create a simple daisy chain loop of a M580 HSBY ePAC system.
- 

### Determine which CPU to be setup as A or B (You will need the Equipment for this part of the exercise)

- i. Before starting any HSBY project, we need to decide which CPU to be setup as CPU A or CPU B.

On the back of the processor module, you will find a small rotary switch.



- ii. Decide one of the HSBY CPU to be designated as **A**. Use a small screwdriver provided and change the rotary switch pointing to position **A**.
  - iii. Mount this **CPU A** onto the CPU local rack.
  - iv. The other HSBY CPU will be designated as **B**. Similarly, use a small screwdriver and change the rotary switch position to **B**.
  - v. Mount this **CPU B** onto the other CPU local rack.
- 



### Hints & Tips

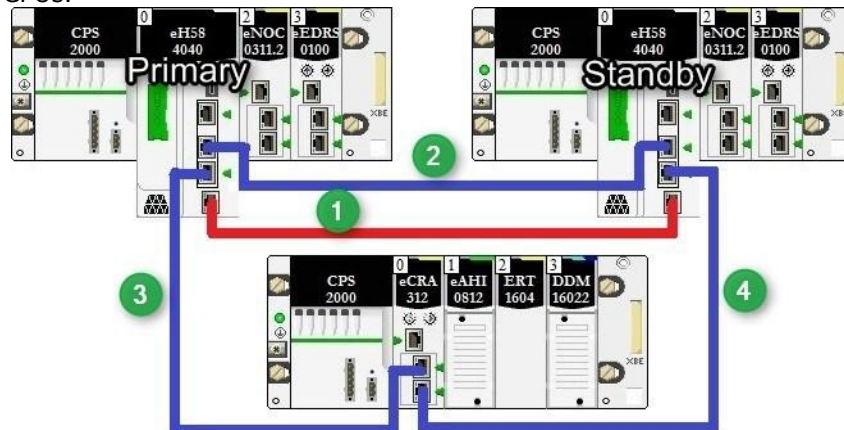
This procedure is important to identify the physical processor module in Hot Standby system architecture.

---

## Exercise – Hardware Setting (cont.)

### Connect the hardware for a daisy chain loop

- i. Use a patch cable to link the two dedicated ports of the two M580 HSBY CPUs.



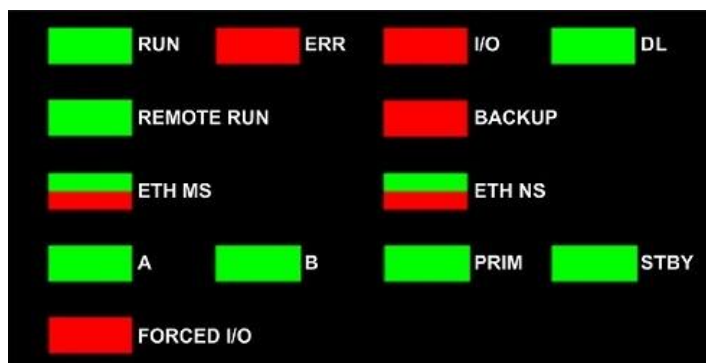
- ii. Take a long Blue patch cable and connect the ETH2 port from the Primary M580 HSBY CPU to the ETH2 port on the Standby M580 HSBY CPU.
- iii. In the same manner, use another short Blue patch cable and connect the ETH3 port from the Primary M580 HSBY CPU to the ETH2 port on the BME CRA 312 10.
- iv. To make a simple daisy chain loop, loop back by connecting the ETH3 port of the BME CRA 312 10 to the ETH3 port of the Standby M580 HSBY CPU.
- v. Switch both PLCs to ON.
- vi. Observe the LEDs behaviour of both M580 HSBY CPUs and the CRA module.

## Exercise – Hardware Setting (cont.)

---

### Test the functioning of the M580 HSBY

- i. Locate the Primary CPU Using the LED panel on the CPU:



- ii. Unplug all cables from this CPU.
- iii. Check that the other CPU is now Primary.
- iv. The exercise is now over click the link to go back to the [Chapter 2 Organisation Chart](#) or to the [Table of Contents](#).

